

**Amendments to the Specification:**

Please replace the paragraph beginning at page 10, line 15 and extending to page 11, line 10 with the following rewritten paragraph:

For example, when process 402 opens socket 412 to read data from process 404 via socket 414, the read calls 416 are interposed with the Speed Library that enables doors 406 and 408. The Speed Library enables processes 402 and 404 communicate synchronization signals via the doors 406 and 408 through kernel 418. The mapped memory 410 enables data to transfer from process 404 to process 402 based on the synchronization signals without interfering with the kernel 418. When process 402 opens socket 412 to write data through socket 414 to process 404, the write calls 420 are interposed with the Speed Library that enables doors 406 and 408. The Speed Library enables processes 402 and 404 communicate synchronization signals via the doors 406 and 408 through kernel 418. The mapped memory 410 enables data to transfer from process 404 to process 402 based on the synchronization signals without interfering with the kernel 418. Both processes 402 and 404 are represented in the user space 422 while the kernel 418 is represented in the kernel space 424. Thus, the ~~sockets~~ processes 402 and 404 virtually communicate (represented by line 426) while the data and synchronization signals are actually transferred through the mapped memory 410 and doors 406, 408 respectively enabled by the Speed Library.